Substitute for form 1449/PTO	Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT *(Use as many sheets as necessary)*	Application Number	10/643,050
	Filing Date	08/18/03
	First Named Inventor	James H. Kukula
	Art Unit	2825
	Examiner Name	ROSSOSHEK, YELENA
Sheet	1	of	1	Attorney Docket Number	SNPS 0504

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No.[1]	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T[2]
	1	"Representing circuits more efficiently in symbolic model checking." Burch, J.R.; Clarke, E.M.; Long, D.E. ACM/IEEE 28th Design Automation Conference, 1991. Publication Date: June 17-21, 1991. On page(s): 403 – 407.	
	2	"Efficient Modal Checking by Automated Ordaring of Transition Relation Partitions." Danial Geist , Ilan Baer. Proceedings of the 6th International Conference on Computer Aided Verification. Publication Date: June 21-23, 1994. On page(s): 299-310.	
	3	"Early quantification and partitioned transition relations." Hojati, R.; Krishnan, S.C.; Brayton, R.K. Proceedings, IEEE International Conference on Computer Design (ICCD): VLSI in Computers and Processors, 1996. Publication Date: 7-9 Oct. 1996. On page(s): 12 - 19.	
	4	"Bordar-Block Triangular Form and Conjunction Schedule in Image Computation." In-Ho Moon , Gary D. Hachtal , Fabio Somanzi. Proceedings of the Third International Conferance on Formal Methods in Computar-Aided Design. Publication Date: November 01-03, 2000. On page(s): 73-90.	
	5	"Efficient BDD algorithms for FSM synthesis and varification." R. K. Ranjan, A. Aziz, R. K. Brayton, B. F. Plessier, and C. Pixley. International Workshop for Logic Synthesis, May 1995. Lake Tahoe, CA.	
	6	"Implicit state anumeration of finite state machines using BDD's." Touati, H.J., Savoj, H., Lin, B., Brayton, R.K. and Sangiovanni-Vincentelli, A. IEEE International Conference on Computer-Aided Design (ICCAD), 1990. Digest of Technical Papers. Publication Date: 11-15 Nov. 1990. On page(s): 130 – 133.	

Examiner Signature		Date Considered	